

INTEGRATED CIRCUIT AND METHOD OF CONTROLLING OUTPUT IMPEDANCE

Abstract of the Disclosure

5 An integrated circuit (100, 200, 300) includes a voltage-mode driver circuit having an analog on-chip termination and also having a substantially constant output impedance across an operating range of an output voltage of the voltage-mode driver circuit. The voltage-mode driver circuit also maintains a substantially constant output impedance through voltage transitions to minimize voltage reflections while driving cabling.

10